METHOD, SYSTEM, AND PROGRAM

FOR HANDLING DEVICE

INTERRUPTS IN A MULTI-PROCESSOR ENVIRONMENT

ABSTRACT

5   **[0036]**   Provided are a method, system, and program implemented by a device driver executing in a computer for handling interrupts from an associated device, wherein the device driver is capable of interfacing with the associated device. The device driver periodically monitors usage of the processors in the system and pins a processor to execute the interrupt handler of the device driver based upon the monitored usage. If the usage of the pinned

10   processor exceeds that of one or more of the other processors, the device driver may pin the interrupt handlers to a different, lower utilized processor.